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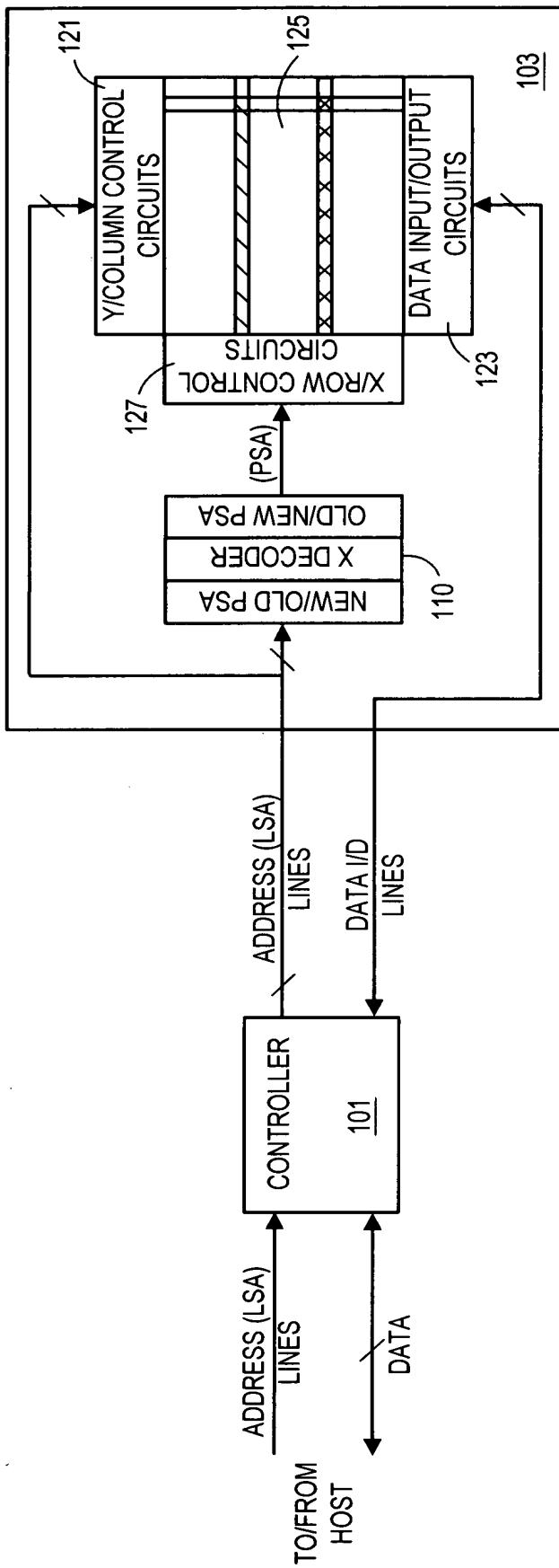


Figure 1

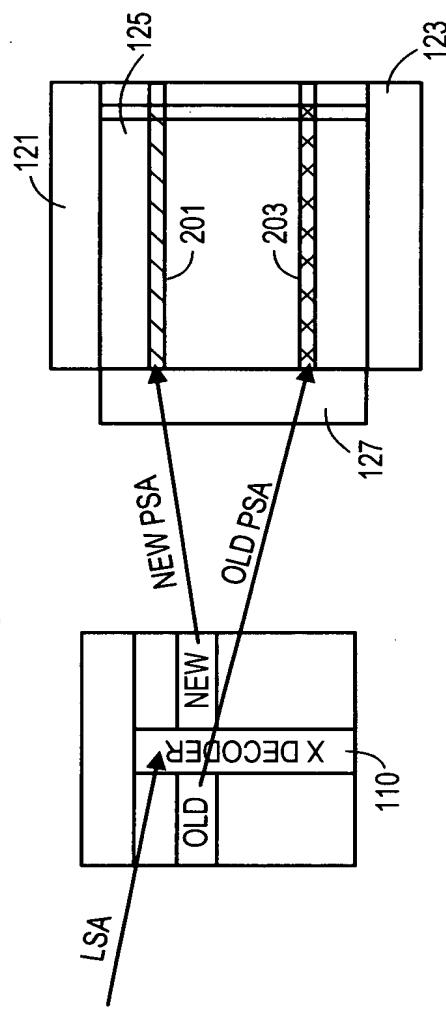


Figure 2

LSA	110	PSA	125	321	323
1	1				
2	2				
3	3				
4	4				
5	5				
6	6				
7	7				
8	8				
		x			
			x		
				x	
					x

Fig. 3a

LSA	110	PSA	125	321	323
1	1				
2	2				
3	3				
4	4				
5	5				
6	6				
7	7				
8	8				
		x			
			x		
				x	
					x

Fig. 3d

LSA	110	PSA	125	321	323
1	1	A A A A A A A A			
2	2	B B B B B B B B			
3	3	X X X X X X X X			
4	4				
5	5	C C C C C C C C			
6	6				
7	7				
8	8				
		x			
			x		
				x	
					x

Fig. 3b

LSA	110	PSA	125	321	323
1	1	A A A A A A A A			
2	2	B B B B B B B B			
3	3	G G G G G G G G			
4	4				
5	5	C C C C C C C C			
6	6	D D D D D D D D			
7	7	E E E E E E E E			
8	8	F F F F F F F F			
		x			
			x		
				x	
					x

Fig. 3c

LSA	110	PSA	125	321	323
1	1	A A A A A A A A			
2	2	B B B B B B B B			
3	3	X X X X X X X X			
4	4				
5	5	C C C C C C C C			
6	6	E E E E E E E E			
7	7				
8	8				
		x			
			x		
				x	
					x

Fig. 3e

LSA	110	PSA	125	321	323
1	1	A A A A A A A A			
2	2	B B B B B B B B			
3	3	G G G G G G G G			
4	4				
5	5	C C C C C C C C			
6	6	E E E E E E E E			
7	7				
8	8				
		x			
			x		
				x	
					x

Fig. 3f

Memory Device With Sector Pointer Structure

Inventor(s): Raul-Adrian Cernea

Application #: 10/071,795

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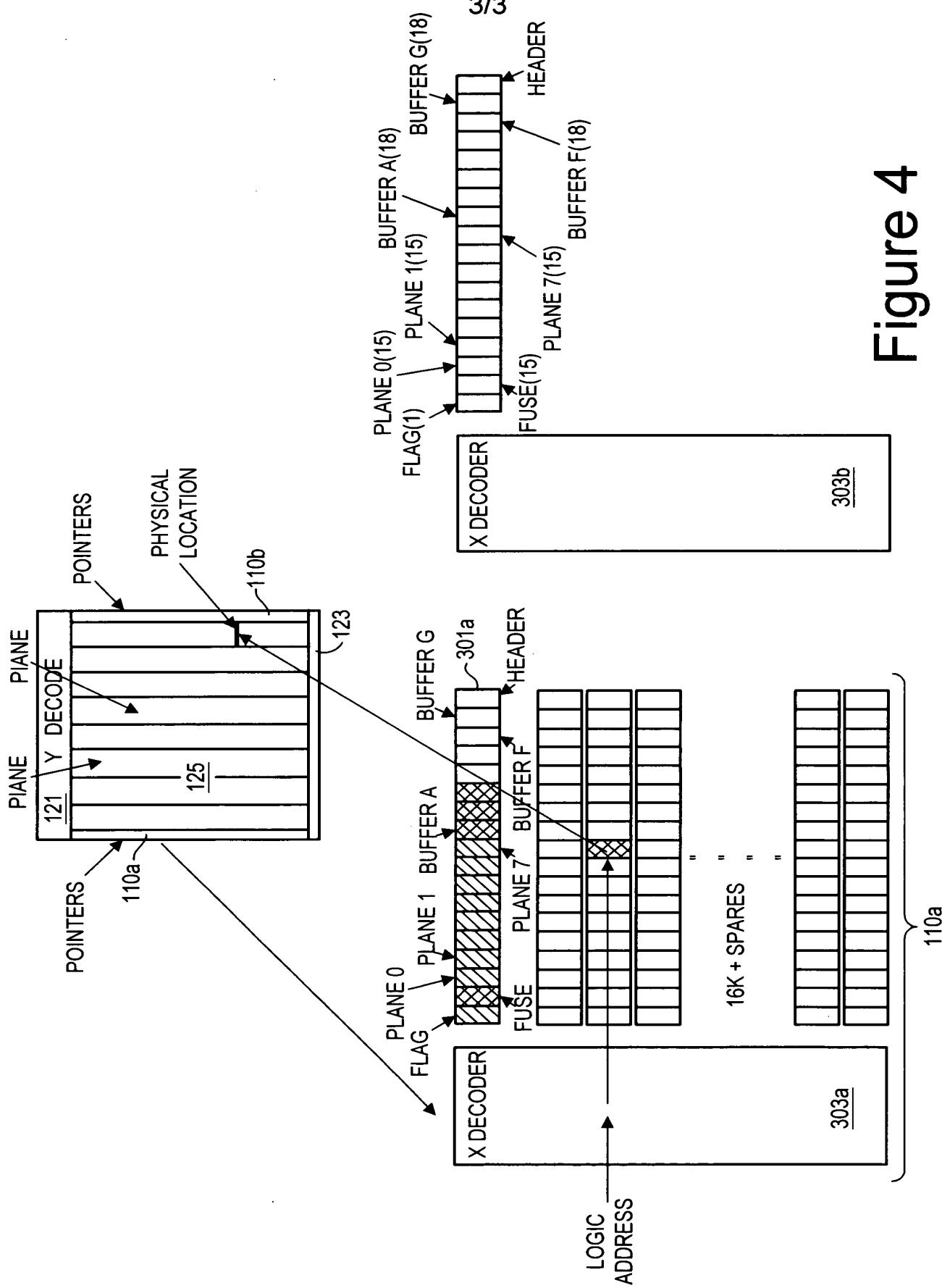


Figure 4